

MR2919-10  
Appl. No. 10/012,209  
Suppl. Amdt. Dated 18 November 2003  
Reply to Office Action of 8 July 2003

IN THE CLAIMS:

This Listing of Claims will replace all prior versions, and listings, of Claims in the subject Patent Application. For clarity, the markings shown in the pending Claims are as they were shown in the 30 October 2003 Amendment After Final, which this Supplemental Amendment supplements. The Claims showing the markings remain labeled "(Currently amended)" as they were in the 30 October 2003 Amendment After Final.

Listing of Claims:

1-7. (Cancelled).

8. (Currently amended) An on-chip signal transforming device, the device comprising:

    a substrate;  
    a first conductive layer above the substrate, wherein the first conductive layer has formed thereon a first plurality of inductors disposed in interleaved manner inductors, the first plurality of interleaved inductors formed completely within the first conductive layer; and

    a second conductive layer above the substrate insulated from the first conductive layer, wherein the second conductive layer has formed thereon at least one

MR2919-10  
Appl. No. 10/012,209  
Suppl. Amdt. Dated 18 November 2003  
Reply to Office Action of 8 July 2003

inductor DC isolated from the first plurality of interleaved inductors, the at least one inductor formed completely within one or more layers other than the first conductive layer.

9. (Original) The device of claim 8, wherein the second conductive layer is below the first conductive layer.

10. (Previously presented) The device of claim 8, wherein the at least one inductor is a second plurality of interleaved inductors.

11. (Original) The device of claim 8, wherein the at least one inductor is a spiral inductor.

12. (Previously presented) The device of claim 9, further comprising:  
a third conductive layer above the substrate insulated from the first and second conductive layers, wherein:

the at least one inductor is also partly formed in the third conductive layer, and

the third conductive layer is above the first conductive layer.

13. (Currently Amended) An on-chip signal transforming device, the device comprising:

MR2919-10  
Appl. No. 10/012,209  
Suppl. Amdt. Dated 18 November 2003  
Reply to Office Action of 8 July 2003

a substrate;

a first conductive layer above the substrate, wherein the first conductive layer has formed thereon a first plurality of inductors disposed in interleaved manner inductors, the first plurality of interleaved inductors formed completely within the first conductive layer; and

a second conductive layer above the substrate insulated from the first conductive layer, wherein the second conductive layer has formed thereon a second plurality of interleaved inductors DC isolated from the first plurality of interleaved inductors, the second plurality of interleaved inductors formed completely within the second conductive layer.

14. (Currently Amended) An on-chip signal transforming device, the device comprising:

a substrate;

a first conductive layer above the substrate, wherein the first conductive layer has formed thereon a first plurality of inductors disposed in interleaved manner inductors, the first plurality of interleaved inductors formed completely within the first conductive layer;

a second conductive layer above the substrate, and below and insulated from the first conductive layer, wherein the second conductive layer has formed

MR2919-10  
Appl. No. 10/012,209  
Suppl. Amdt. Dated 18 November 2003  
Reply to Office Action of 8 July 2003

thereon at least one inductor DC isolated from the first plurality of interleaved inductors,  
the at least one inductor formed completely within one or more layers other than the first  
conductive layer;

a third conductive layer above the first conductive layer and  
insulated from the first and second conductive layers, wherein the at least one inductor is  
also partly formed in the third conductive.

15-18. (Canceled).

19. (Currently amended) An on-chip signal transforming device, the device  
comprising:

a substrate;  
a first conductive layer above the substrate, wherein the first  
conductive layer has a first spiral inductor and a second spiral inductor that are disposed  
in interleaved manner, the first spiral inductor formed substantially within the first  
conductive layer and the second spiral inductor formed completely within the first  
conductive layer;

a second conductive layer above the substrate insulated from the first  
conductive layer; and

MR2919-10

Appl. No. 10/012,209

Suppl. Amdt. Dated 18 November 2003

Reply to Office Action of 8 July 2003

a conductive path from the first conductive layer to the second conductive layer and back to the first conductive layer to allow the first spiral inductor to cross over the second spiral inductor.

20. (Previously presented) The device according to claim 19, further comprising:  
a third conductive layer above the substrate and insulated from the first and second conductive layers, wherein the third conductive layer has a third spiral inductor that inductively couples to the first and second spiral inductors, the third spiral inductor formed completely within the third conductive layer.